

```

to_gray_process(gsr, mclk)
begin
  if(gsr == '1') then
    in_cnt_gray[0:1] <= others == '0';
  else if(rising_edge(mclk)) then
    in_cnt_gray[0:1] <= in_cnt_gray[0:1];
    for i in 0 to ADDR_WIDTH - 2 loop
      in_cnt_gray[ADDR_WIDTH - 2 - i] <= in_cnt_gray[ADDR_WIDTH - 2 - i];
    end loop;
  end if;
end process;

begin
  if(gsr == '1') then
    fifo_elem_cnt <= (others == '0');
  elsif rising_edge(mclk) then
    process(gsr, mclk)
    begin
      if(gsr == '1') then
        fifo_elem_cnt <= (oth

```

logiCRAFT3

Compact Multimedia Display Development Platform

The logiCRAFT3 is a small-factor and field-configurable platform based on the logiCRAFT2 platform. It is designed to support a wide variety of audio and video sources and handle a variety of display types including today popular COG displays. The logiCRAFT3 integrates several standard communication interfaces enabling an easy programming and control, as well as a smooth integration into larger systems.

The logiCRAFT3 platform is primarily aimed at the automotive market, including applications such as navigation, infotainment, rear-seat entertainment (RSE), and driver assistance. Other multimedia applications are equally applicable, such as consumer, medical, and measurement instrumentation or factory automation applications.

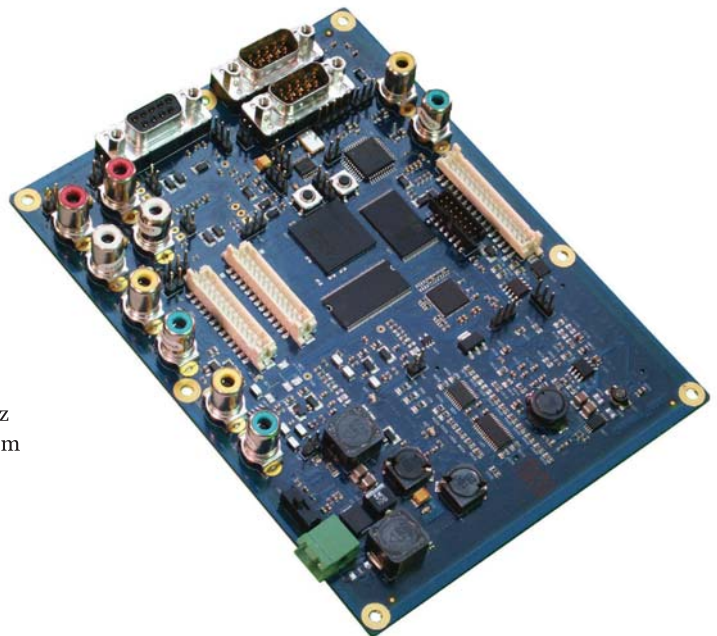
In order to allow an easy and quick evaluation of Xylon demo designs and development of the custom projects, the logiCRAFT2 fully supports a highly sophisticated Xilinx Embedded Development Kit (EDK) development flow.

TARGETED MARKETS

- Automotive
- Consumer
- Medical
- Industrial
- Military
- Aerospace

KEY FEATURES

- Small form factor 155 x 115 mm (4" x 3")
- Xilinx Spartan-3E 3S250E/500E/1200E 256-pin FPGAs (Spartan 3E-500E available on demand)
- 8MB parallel NOR FLASH
- 16MB/32-bit wide SDRAM running up to 133MHz
- Up to two simultaneous video inputs selectable from four CVBS or two S-Video PAL/NTSC/SECAM inputs
- One LVDS channel (clock + 3 data pairs) directly connected to FPGA
- High speed LVDS Gbit/s digital transceiver
- 8 bit general purpose I/O
- Touch screen controller
- CAN interface, and single wire CAN interface
- RS232 interface
- Interface to external Bluetooth module
- iPod control interface
- Four stereo line inputs
- Microphone input
- Audio line output
- Headphones output
- Two stereo IR headphone audio outputs
- Video output configurable as:
 - CVBS / S-VIDEO PAL/NTSC/SECAM output
 - One LVDS channel (clock + 3 data pairs) directly from FPGA
 - Digital RGB interface
- Power supply for COG displays including GAMMA correction and VCOM circuits
- Power and control output for CCFL backlight inverters, and LCD backlights
- Installation CD containing:
 - Evaluation demo project prepared for the Xilinx EDK
 - Demo application software in C source code
 - Design and software quick-loader application
 - PCB schematics, BOM, etc.



PLATFORM DELIVERABLES

- logiCRAFT3 PCB*
- MicroBlaze™ based EDK demo including evaluation logicBRICKS™ IPs
- SW demo application (C source) including drivers for logicBRICKS™ IPs
- Design loader application
- Full platform schematics
- Documentation

* Various platform assemblies available. Please check the product's data sheet or contact Xylon directly.

www.logicbricks.com

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